

# New Release

### LTEC Corporation

Your most experienced partner in IP protection

#### HANGZHOUEV-TECH ON-BOARD CHARGER CIRCUIT ANALYSIS REPORT

**February 2020.** This is a detailed circuit analysis report of the Hangzouev-tech's on-board charger unit found in Great Wall Motor Co's ORA-R1 electric vehicle.





ORA-R1 of Great Wall Motor Co. Ltd.

The On-board charger unit

This vehicle is the lowest grade model among the EC cars produced by Great Wall Motor Co. The 33 kWh batteries are mounted under the floor of the vehicle. The peak output power of the Front-Engine Front-Drive (FF) system is 35kW (about 48hp).

#### **OBC** unit features

The OBC has a three-layer housing. The main elements of the upper layer are the junction box board (JB) and a power relay. The middle layer consists of a charger and a DC-DC converter circuit composed of a control board and a main board. A cooling channel is provided within the lower layer (bottom side of the housing). The OBC also has PFC, LLC converter, and a 14V DC-DC converter.

### **Report content**

The 96-page report provides PCB layout of each layer, function block diagram, detailed circuit diagram, and parts list. A layout vs. schematic viewer is also provided.

Note: The report price may change over time. For current price contact info@ltecusa.com.

19G-0014-2



Phone: (408) 489-1994 www.ltecusa.com Contact: info@ltecusa.com

## **Table of Contents**

|   | Page |
|---|------|
| Summary   |      |
| Table 1, Product outline                                  | 3    |
| Table 2, PCB outline                                      | 4    |
| Analysis result summary                                   |      |
| Fig 1. OBC outline  | 9    |
| Fig 2. OBC product identification                         | 10   |
| Fig 3. Teardown process documentation                     | 11   |
| Fig 4. PCB outline (JB, controller and main PCB)          | 32   |
| Fig 5. PCB X-Ray (JB, controller and main PCB)            | 35   |
| Fig 6. PCBs after component removal                       | 38   |
| Fig 7. PCB layout of each layer (controller and main PCB) | 41   |
| Components position                                       |      |
| Fig 8. Components position (JB, controller and main PCB)  | 43   |
| Component   |      |
| Table 3. Component summary                                | 55   |
| Fig 9. High magnification images of each component        | 56   |
| <u>Interface</u>  |      |
| Fig 10. Connectors  | 61   |
| <u>Circuit</u>  |      |
| Fig 9-1. Function block diagram                           | 67   |
| Fig 9-2. Component-level schematic                        | 69   |
| BOM list  | 70   |



Phone: (408) 489-1994 www.ltecusa.com Contact: info@ltecusa.com