



**Market Report:
Power-Supply-in-a-Package**

PSIP

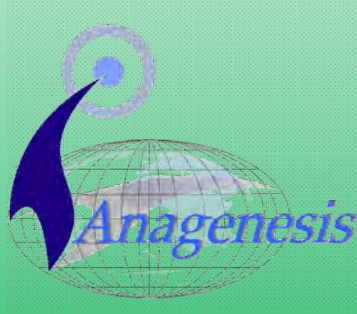
and

Power-Supply-on-Chip

PwrSoC

**The Power Density Challenge Continues
2015 – 2021**

**With market analysis from
Anagenesis Inc.**



Copyright © LTC Corporation 2018 – all rights reserved

Copyright © LTC Corporation 2018 – all rights reserved

Market Report: PSiP & PwrSoC – The Power Density Challenge Continues 2015 – 2021

Now with product and technology details from LTEC

Collaborating with LTEC Corporation, Anagenesis is augmenting its forecast/technology report with LTEC's device and construction benchmarking which provides deeper insight into the micro-construction of present and potential PSiPs & PwrSoCs.

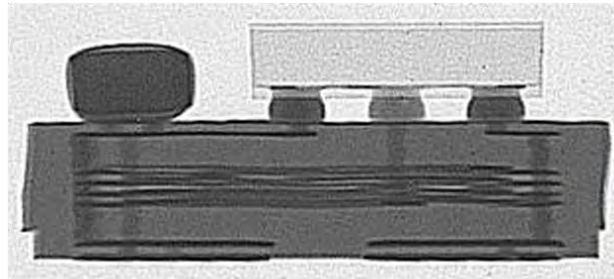
Power Supply in a Package (PSiP)

Encapsulated PSiPs continue to experience double digit growth capturing enough significance to shrink the open PC Board design share of market.

Demand for higher density, low noise solutions will feed growth for 2018 and beyond.

Mergers and acquisitions have created great opportunities for competitive players.

Magnetic designs are meeting customer demand for lower PSiP profiles.

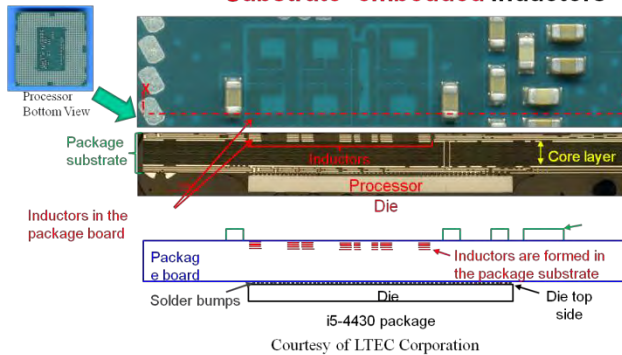


3-Layer Inductor element

X-ray of side of the PSiP

Courtesy of LTEC Corporation

Haswell processor with Substrate- embedded inductors



Courtesy of LTEC Corporation

Power Supply on a Chip (PwrSoC)

While granular technology had a bold commercial launch in 2014, where are they today?

Who will be first to have a sustainable commercial modular PwrSoC? Do L-based or C-based topologies have a stronger future?

Are mergers and acquisitions helping or hindering successful advancement?

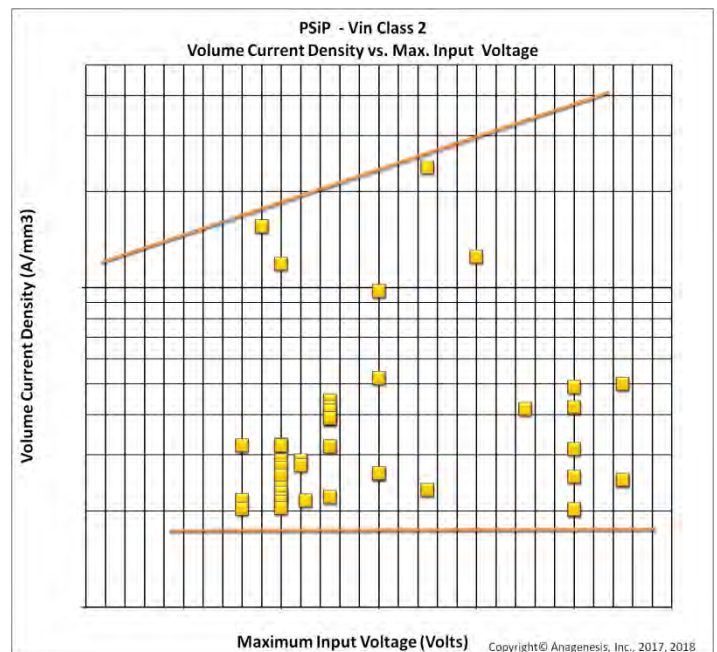
Will all PwrSoC opportunities be captive?

What impact will embedded granular PwrSoC have on external POLs?

Why read this report

Cut through all the buzz and hype surrounding on PSiP and PwrSoC. Get the focused perspective and reality check needed to understand these products and their dynamic markets.

- Save time with this unique technical-marketing tool
- Appraise the technical readiness of 33 companies
- Analyze ten-year trends of over 351 products
- Assess influencing technologies & applications



Learn

- What are the future growth drivers and high growth applications?
 - What is so challenging about successful entry into the PSiP market
 - What technologies will enable the next generation PwrSoC and PSiP?
 - Who are the successful PSiP players and who will be forthcoming?
 - Why are the miniature open construction PSiPs gaining so much growth?
 - What makes the PwrSoC opportunity different from PSiP?
 - How is PwrSoC more than an engineer's dream in search of a market?
 - Why has it been so difficult for the PwrSoC to become a reality?
 - How fast will the PwrSoC market grow and will it continue to drive additional investments?
 - Where do your best emerging opportunities exist?
-

Content

Chapter I – Introduction

Chapter II – Executive Summary

Chapter III – Purpose, Scope, and Methodology

Chapter IV – Recent Key Developments and Milestones

Chapter V – PSiP Market Analysis and Trends

Applications & Drivers

Product Comparisons and Trends (over 11 years)

By Product Types & Package Types

Figures of Merit – Density, Efficiency, Pricing

Technical Benchmarks – Switching Frequency

By Voltage Classes

Future Enabling Technologies

Control Method, Power Switches, Power Magnetics, Power Capacitors, & Packaging

Chapter VI – PwrSoC Market Analysis & Trends

Evolution to the PwrSoC

Parametric Progress toward PwrSoC

Emerging PwrSoC Applications

Future Enabling Technologies

Control Method, Semiconductor Switches, Integrated Magnetics, Integrated Capacitors, and Packaging

Modular & Granular PwrSoCs, L and C based

Chapter VII – PSiP Competitive Market Analysis

Market Revenue Share by Company

Product Introduction Activity (2009 – 2016)

Product Landscape

Technology Readiness Assessment

49 Company Profiles: Strengths and Weaknesses

Key Alliances

Market Barriers and Customer Issues

Chapter VIII – Forecasts: 2015 to 2021

Forecast Assumptions and Methodology

The Miniature Power Supply Product Forecast

PSiP Market Forecast (Revenue, Quantity, and Price)

By Product Type

By Package Type

By Voltage Class

by Market Subsector

Upside Estimate Possibilities

PwrSoC Forecasts (Modular & Granular)

Chapter IX – Recommendations

To the Manufacturers, Users, Component Suppliers

Appendices

Appendix A – References

Appendix B - Definitions



The Anagenesis/LTEC Difference

Anagenesis has investigated and reported the evolving PSiP and PwrSoC markets since 2007. Such long-time, in-depth research qualifies Anagenesis to offer a unique objective perspective. Moreover, we have the engineering expertise and business relationships to take the PSiP and PwrSoC customers' perspective. As a result, previous customers say our reports are "deeply impressive" and "have details unavailable anywhere else."

LTEC Corporation is Japan's leading, well-respected supplier of products and technologies teardown and deep analysis (benchmarking) for IP protection and new product R&D for over three decades. LTEC offers teardowns, deep analysis of competing products and technologies for product development, patents' enforcement and defense with headquarters in Osaka, Japan, and local offices in the USA and the Asia-Pacific region.



Order this market report today.

Report Price: \$4,995.00 USD

Anagenesis Inc., Suite 2000, 222 N. Sepulveda Blvd., El Segundo, CA 90245 info@anagenesis-inc.com
Arnold Alderman Phone: 310 662 4764 Mobile: 310 704 7079 Ada Cheng Phone: 310 406 5691

Copyright© Anagenesis, Inc. 2018 All rights reserved